
Logic Synthesis Using Synopsys® Pran Kurup

*state machine coding styles for synthesis - sunburst design - snug 1998 state machine coding styles for synthesis rev 1.1 2 introduction steve golson's 1994 paper, "state machine design techniques for verilog and vhd" [1], is a **nonblocking assignments in verilog synthesis, coding ...** - snug san jose 2000 nonblocking assignments in verilog rev 1.4 synthesis, coding styles that kill 4 4.0 nonblocking assignments the nonblocking assignment operator is the same as the less-than-or-equal-to operator ("*